Republic of North Macedonia

Ministry of Transport

## Western Balkans Trade and Transport Facilitation Project

## CLARIFICATION NO. 5

## To the Request for Bids for the procurement of

## Design, Supply and Installation of the New Single Window (NSW) System in North Macedonia

## Ref. No.: WBTTFP-8929-MK-112C-RFB

Issued on date: August 08, 2024

To all prospective Suppliers and to all firms that have obtained the RFB documents

Dear All,

With reference to the question raised by the prospective Bidders and pursuant to the issued RFB for the stated subject, please find enclosed the Clarification Table below with the questions and answers.

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| --- | --- | --- | --- | --- | --- |
| ***CLARIFICATION TABLE*** | | | | | |
| **No. of Question** | **Question** | **Ref. to the BD (RFB)** | **No. of Answer** | **Answer** | **Category:**  **Clarification or Amendment** |
| 1 | **Q:**  Considering the requirement 1.5 General Compute Servers – Hypervisor hosts - Qty. 4 + 1 (cluster for Primary site + single for DR Site), based on the existing model of the market and other technical requirements, please confirm that this requirement refers to 8 memory channels (per CPU) and in total 16 for 2 CPU’s could be offered? | Section VII | 1 | **A:**  We confirm that the requirement for General Compute Servers – Hypervisor hosts (Qty. 4 + 1) for the Primary site and Disaster Recovery (DR) site, as specified in requirement 1.5, refers to servers with 8 memory channels per CPU, totaling 16 memory channels for 2 CPUs. This specification aligns with the existing model of the market and other technical requirements. | Clarification |
| 2 | **Q:**  Considering the requirement *11.8 Backup Server*, based on the existing model of the market and other technical requirements, please confirm that this requirement refers to *8 memory channels (per CPU) and in total 16 for 2 CPU’s could be offered?* | Section VII | 2 | **A:**  We confirm that the requirement for the Backup Server, as specified in requirement 11.8, refers to servers with 8 memory channels per CPU, totaling 16 memory channels for 2 CPUs. This specification is in accordance with the TOR and aligns with the existing model of the market and other technical requirements. | Clarification |
| 3 | **Q:**  Considering the requirement *11.1 Server Rack with equipment - Rack PDUs, Input Connections: IEC 60309 32 A 2P + E,* the requirement for the Rack PDUs for the Input Connections are 32A. Please confirm that IEC 60309 20 A 3P+N+E type of connection could be offered as equivalent to the requirement? | Section VII | 3 | **A:**  We confirm that the requirement for Server Rack with equipment - Rack PDUs, as specified in requirement 11.1, which mentions Input Connections: IEC 60309 32 A 2P + E, can be met with the IEC 60309 20 A 3P+N+E type of connection as an equivalent. This confirmation is in accordance with the requirement and aligns with the existing market availability and technical requirements. | Clarification |

**Please note that confirmation by e-mail of the receipt of this Clarification no. 5 is compulsory!**

Yours sincerely,

**Slavko Micevski**

Procurement officer

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